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No. EL/4.2.15/GEN

Principal Chief Electrical Engineer Central Railway Mumbai CSTM Dated: 23.04.2019

Sub: Problem faced in operation of triple Loco in MU formation.

Ref: (i) Central Railway's letter no. L.253.AC.17 Vol. II dated 12.06. 2018.

(ii) Central Railway's letter no. L.253.AC.17 dated 01.01. 2019.

- Central Railway vide letter under reference (i) had informed that MS-442 revision '0' dated 11.09.2015 to overcome the problem of notch non-synchronisation is applicable for multi operation of two locomotives. Central Railway had requested to issue necessary guidelines for triple Loco operation.
- 2. RDSO had already issued modification sheet no. RDSO/2018/EL/MS/0442 rev. '1' dated 1.8.2018, for GR-0 indication of middle and trailing locomotives through additional signalling lamp in leading locomotive in multiple unit/consists operation for two/three locomotives.
- 3. The issue of non-synchronization of notches during MU operation was already studied and RDSO had issued following instructions vide letter number EL/4.2.15 dated 22.07.2013, 22.08.2014, 14.05.2018.

"Railways have reported difference of one to two notches in leading and trailing locomotives, during multiple/consists operation in MPCS fitted tap changer locomotives. This phenomena is more observed during notch by notch regression. The problem is experienced in conventional tap changer locomotives fitted with relays. also. This is because of inherent limitation in existing locomotive circuit. Normally, loco pilot put MP on zero before braking, in conventional tap changer locomotive fitted with relays.

Same action is required in MPCS fitted tap changer locomotives, also, i.e. putting MP on '0' before braking".

4. Therefore, following instructions may please be re-iterated to loco pilots:

"In MU/Consists operation, before braking, ensure MP on Zero position so that notches in leading and trailing locomotives become zero."

5. It is also requested to expedite the implementation of MS 442 revision '1'.

(P.K. Saraswat)

For Director General/Elect.

Encl: nil